Fabrication of nano-crystalline silicon thin film at low temperature by using a neutral beam deposition method

Se-Koo Kang a, Min-Hwan Jeon a, Jong-Yoon Park a, Hyoung-Cheol Lee b, Byung-Jae Park b, Je-Kwan Yeon b, Geun-Young Yeom a,b,*

a SKKU Advanced Institute of Nano Technology (SAINT), 300, Chunchun-dong, jangan-gu, Suwon, Gyeonggi-do 440-746, Republic of Korea. Tel.: +82 31 299 6562; fax: +82 31 299 6565.
E-mail address: gyyeom@s sku.edu (G.-Y. Yeom).

b Department of Advanced Materials Science & Engineering, Sungkyunkwan University, Suwon, Gyeonggi-do 440-746, Republic of Korea.

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ABSTRACT
Low temperature (≈80 °C) neutral beam deposition (LTNBD) was investigated as a new approach to the fabrication and development of nano-crystalline silicon (nc-Si), which has better properties than that of amorphous silicon (α-Si). The difference between LTNBD and conventional PECVD is that the film formation energy of the nc-Si in LTNBD is supplied by controlled neutral beam energies at a low temperature rather than by heating. Especially, in this study, the characteristics of the nc-Si thin film were investigated by adding 10% of an inert gas such as Ne, Ar or Xe to SiH 4/H 2. Increasing the beam temperature resulted in an increase in the deposition rate, but the crystallinity was decreased, due to the increased damage to the substrate. However, the addition of a higher mass inert gas to the gas mixture at a fixed beam energy resulted not only in a higher deposition rate but also in a higher crystallization volume fraction. The high resolution transmission electron microscopy image showed that the grown film is composed of about 10 nm-size grains.

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1. Introduction

Crystalline silicon (c-Si) thin films (nano-crystalline silicon: nc-Si, or microcrystalline silicon: μc-Si) are generally applied to electronic and optoelectronic devices, because of their higher carrier mobility than that of amorphous silicon (α-Si) films and excellent potential in fabricating higher speed and higher resolution thin film transistor–liquid crystal displays (TFT–LCDs) or active matrix organic light emitting diodes (AMOLEDs). In addition, c-Si thin films can provide higher electrical conductivity than α-Si and have a low possibility of the degradation of their electrical properties, resulting from the loss of hydrogen in the amorphous silicon. Currently, c-Si thin films are fabricated by crystallizing α-Si films deposited by plasma enhanced chemical vapor deposition (PECVD) with a post-treatment method such as excimer laser annealing (ELA), solid phase crystallization (SPC), metal-induced crystallization (MIC), etc. [1–4]. However, these troublesome post-treatments require a high processing temperature (500–600 °C), which is higher than the glass transition temperature. Furthermore, throughput and cost issues become more critical as the substrate size is increased.

If c-Si were able to be deposited on plastic substrates at a low temperature, it would reduce the fabrication cost significantly and this technique could be used for next generation displays such as flexible displays. Also, a technique that allows for c-Si TFTs to be fabricated on plastic substrates could be applied to the fabrication of other electronic devices such as smart card, new non-volatile memory, and portable electronics. Therefore, to simplify the process steps and increase the production throughput, the direct deposition of c-Si at a low temperature (usually nc-Si not μc-Si at a low temperature deposition) is definitely required.

nc-Si:H thin films can be directly deposited by conventional PECVD at a low temperature with very high H 2 percentages in the SiH 4/H 2 gas mixture, but it is known that the deposition rate must be kept extremely low to obtain a reasonable crystallization volume fraction. Therefore, for the direct deposition of nc-Si:H at a low temperature, alternative ideas have been proposed instead of the conventional PECVD method [5], such as ion beam irradiation or substrate biasing during the deposition of the silicon film in order to use the kinetic energy of the bombarding ions incident on the growing film surface for crystal growth [6–9]. Bae et al. [6] reported that the rf-substrate bias increases the
crystallinity of the silicon deposited by the electron cyclotron resonance PECVD method. Swain et al. [7] also reported that the application of a negative bias increases the crystallite size and crystalline fraction of the nc-Si deposited by the hot-wire CVD. Nozawa et al. [8] studied the effect of substrate bias on crystallinity from the point of view of ion energy, ion flux, and substrate temperature. Saha et al. [9] deposited nc-Si directly at a low temperature by using an ion beam sputtering method and the nc-Si deposited by this technique was compared with that deposited by the plasma-based process.

In this study, as an alternate method of supplying the energy to the particles bombarding the substrate surface during the nc-Si:H deposition at a low temperature, low temperature neutral beam deposition (LTNBD) is investigated. LTNBD is performed by using a gas mixture of SiH₄/H₂ as a deposition source, that is, the deposited films contain a significant fraction of hydrogen atoms. We used a neutral beam instead of an ion beam in the bombardment of the substrate surface, in order to prevent any possible charge-related damage to the growing nc-Si:H thin film. The effect of additive inert gases such as Ne, Ar, and Xe and in some cases, the effect of the energy of the bombarding particles on the characteristics of the grown nc-Si thin films were investigated.

2. Experimental details

Fig. 1 shows a schematic diagram of the neutral beam deposition system used for the fabrication of the nc-Si:H films. The neutral beam source was composed of a 15 cm-diameter inductively coupled plasma (ICP) ion gun with two grids for the acceleration of the reactive ions in addition to the formation of reactive radicals and a low angle parallel plate reflector made of graphite for the neutralization of the reactive ions. The energetic ion beam was extracted from the ICP ion gun by the grid system and the extracted ion beam was neutralized by reflecting on the parallel reflecting plates, which are tilted at an angle of 5° to the ion beam direction for the low angle reflection of the extracted ion beam. The details of the neutral beam deposition system used in the experiment can be found elsewhere [10].

To grow the nc-Si:H thin films, a gas mixture of SiH₄ (10 sccm)/H₂ (80 sccm) was fed to the ICP ion gun source while applying an rf power of 500 W at 13.56 MHz to the ICP source. In addition, in the gas mixture, 10 sccm of an inert gas such as Ne, Ar, or Xe was added to change the characteristics of the deposited film. Corning 1737 glass was used as the substrate and the substrate temperature was kept at room temperature. The operating pressure in the processing chamber was varied from 10 to 40 mTorr and at the first grid of the two-grid system, a voltage of up to 500 V was applied to supply energy to the extracted ions, while the second grid was grounded.

Raman spectroscopy (Kaiser Optical System Inc.) was used to evaluate the crystalline volume fraction of the films with a deposition thickness of 180 nm. The deposited film thickness was measured by a surface profilometer (Tecor, Alpha step-500). The detailed microstructures of the deposited thin film, such as the grain size, were observed by using the high resolution transmission electron microscopy (HRTEM, JEOL JEM 3000F). The dark conductivity of the 180 nm thick films deposited as a function of the additive gas at different first grid voltages was investigated by a semiconductor characterization system (Keithley 4200) after the formation of Al coplanar electrodes on the deposited nc-Si:H.

3. Results and discussion

The deposition rate of the film in the neutral beam deposition system is influenced by several external process parameters such as rf power of the ICP ion gun, process pressure, gas composition, etc. In this experiment, SiH₄ was diluted with H₂ for the deposition of nc-Si:H [11] and the pressure of the process chamber containing SiH₄/H₂ and inert gas species with different atomic masses, viz. Ne, Ar or Xe, added to the SiH₄/H₂ was varied and its effect on the deposition characteristics was investigated. Fig. 2 represents the effect of the process pressure and the first grid voltage on the deposition rate of nc-Si:H for SiH₄-based gas combinations. As the gas combination, SiH₄ diluted with 80% H₂ was used and in the gas mixture, an additive gas, viz. Ne, Ar or Xe, was added at an amount corresponding to about 10% of the total gas flow rate of 100 sccm. In Fig. 2(a), the deposition rates measured as a function of operating pressure varied from 10 to 40 mTorr while the first grid voltage was maintained at 500 V, as are shown. In Fig. 2(b), the deposition rates measured as a function of the first grid voltage from 100 to 500 V at 20 mTorr of operating pressure without the addition of the inert gas [SiH₄ (10 sccm)/H₂ (90 sccm)] and with the addition of Ar [Ar (10 sccm)/SiH₄ (10 sccm)/H₂ (80 sccm)] are shown. As shown in Fig. 2(a), the deposition rate increased with increase in process pressure up to about 20 mTorr, which was controlled by changing the opening aperture size of the throttle valve or various additive gas conditions and further increase in the pressure saturated or slightly decreased the deposition rate. The initial increase of the deposition rate with increase in the process pressure is believed to be related to the increase in the number of radicals and ions with increase in operating pressure in the ICP source. Therefore, deposition rate is related to the increased flux of silicon species on the substrate. However, a slight decrease in the deposition rate with increase in pressure at pressures higher than 20 mTorr is believed to be related to the increased scattering of the ions reflected on the reflector surface. That is, when the ion beam was reflected on the reflector at pressures higher than 20 mTorr, more ions are scattered before the reflection on the reflector, resulting in the decrease of neutral beam flux to the substrate. Therefore, for the following experiments, a process pressure of 20 mTorr was applied.

When the deposition rate was measured with the addition of different inert gases, as shown in Fig. 2(a), it is seen that the addition of an inert gas and especially one with a higher atomic mass increased the deposition rate. The increase of the deposition...
Table 1

<table>
<thead>
<tr>
<th>Atomic Mass</th>
<th>Atomic Radius (Å)</th>
<th>Ionization Potential (eV)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ne</td>
<td>20.179</td>
<td>1.32*</td>
</tr>
<tr>
<td>Ar</td>
<td>39.948</td>
<td>1.88*</td>
</tr>
<tr>
<td>Xe</td>
<td>131.30</td>
<td>2.17*</td>
</tr>
<tr>
<td>SiH4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>H2</td>
<td></td>
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</tr>
</tbody>
</table>

* Standard atomic radius in inert gas configuration.

Ar⁺ or Xe⁺ with silane result in the transfer of electron from silane to the ion [14], which can lead to dissociative ionization of silane. These are known as asymmetric charge exchange reactions resulting in SiHₓ⁺ (0 ≤ x ≤ 3) ions with thermal energy in the neutralizing system. The increase of the deposition rate for the inert gas with the higher mass is believed to be related to the increased plasma density for the inert gas with the higher mass due to the lower ionization potential, which enhances the charge exchange process and surface activation due to the improved plasma density in the ICP source.

As shown in Fig. 2, increase in the first grid voltage also increased the deposition rate, especially when the first grid voltage was increased from 200 to 300 V. Increase of the first grid voltage of the ICP ion source increases the energy of the particles bombarding the surface. Therefore, by supplying energy to the molecules adsorbed on the surface such as silane monomers and oligomers through the transfer of momentum, the resulting energetic particles are able to participate in the reactions as well as in pure chemical reactions. That is, the energetic particles incident on the surface influence the characteristics of the deposition process by forming a reactive region at the topmost surface, while colliding with the surface. However, when the first grid voltage was higher than 300 V, even though the deposition rate was increased slightly, the deposited materials were damaged by physical bombardment.

The crystallization percentage of nc-Si: H deposited by the neutral beam with different additive inert gases, viz. Ne, Ar, and Xe, was estimated by Raman spectroscopy and is shown in Fig. 3. In the SiH4/H2 gas mixture, 10% of Ne, Ar, or Xe was introduced while the other process parameters, viz. the process pressure, rf source power, the first grid voltage, were maintained the same i.e. at 20 mTorr, 500 W, and 500 V, respectively. The Raman spectrum of the nc-Si:H film deposited at a first grid voltage of 300 V with the addition of Ar is also shown (thin line). As shown in Fig. 3, the deposited films were incompletely crystallized since the spectra exhibit two combinations, first a broad band, centered at around 480 cm⁻¹, due to the amorphous silicon portion, and second, a peak near 520 cm⁻¹ (more exactly, near 510 and 518 cm⁻¹) corresponding to the crystalline portion. As shown in these spectra, a change in the additive gas changed the crystallization percentage and the use of an additive inert gas having a higher atomic mass improved the crystallinity of the deposited nc-Si:H film. It has been reported that Xe bombardment is more effective than Ar bombardment, which is generally used in PECVD, in enhancing the film quality [15,16]. This is related to the large mass of Xe as discussed previously in Fig. 2. Also, Xe has a low probability of penetrating into the silicon lattice, due to its large radius, resulting in less damage to the silicon crystal. Furthermore, the concurrent bombardment by a higher mass inert gas can activate the surface layer, thereby allowing the critical activation energy barrier to be exceeded, and increase the surface migration of the deposited silicon atoms. That is, the increase of the crystallinity with increase in mass of the additive gas appears to be related to the increase of the surface atomic mobility afforded by the
increased momentum transfer by the energy of the bombarding particles. As a result, the use of an additive gas and the use of one with a higher mass not only increased the deposition rate but also improved the crystallinity of the deposited film. Decreasing the first grid voltage from 500 to 300 V in the case of Ar addition improved the crystallization percentage significantly from 29% to 44%, indicating that the damage to the grown thin film was decreased at the lower particle bombarding energy.

The dark conductivity of the deposited nc-Si:H films [17–20] was measured by a semiconductor characterization system as a function of the additive inert gas at the first grid voltage of 500 V and the results are shown in Fig. 4. As shown in this figure, the use of a higher mass inert gas as the additive gas increased the dark conductivity from $8 \times 10^{-9}$ S/cm for Ne to $5 \times 10^{-8}$ S/cm for Xe, resulting in an increase of the crystallinity of the deposited nc-Si:H. The dark conductivity of the investigated film was in the conductivity range corresponding to nano-crystalline films.

To identify the crystal structure of the deposited silicon film, HRTEM was used. The micrograph of the silicon film deposited in our experiment with a first grid voltage of 300 V and Ar addition is shown in Fig. 5. As shown in this figure, nc-Si:H having a grain size of about 10 nm embedded in the $\alpha$-Si:H could be observed. Therefore, an nc-Si:H structure was obtained by the neutral beam deposition of silicon at a low substrate-temperature (< 80 °C) without a heating process. From the diffraction pattern, its nano-crystalline structure was also confirmed.

4. Conclusions

In this study, we investigated the possibility of depositing nc-Si:H film at a low temperature (< 80 °C) using a neutral beam. By varying the neutral beam energy and adding various inert gases to a gas mixture of SiH$_4$/H$_2$, nc-Si:H films having different crystallization volume fractions were obtained.

(i) The use of an additive gas, especially one with a higher mass, not only increased the deposition rate but also improved the crystallinity of the deposited film, due to the increase of the surface atomic mobility afforded by the increased momentum transfer.

(ii) The nano-crystalline structure of the deposited film having a grain size of about 10 nm was confirmed by the HRTEM micrograph.

(iii) Increasing the first grid voltage, that is, increasing the neutral beam energy, increased the deposition rate, but decreased the crystalline volume fraction.
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